



Docket No. 030712-23
Serial No. 10/766,896
Page 1

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:)
Makoto TERUI et al.) Group Art Unit: 2811
Serial No. 10/766,896) Examiner: Nittin Parekh
Filed: January 30, 2004)
For: PASSIVE ELEMENT CHIP AND) Date: September 2, 2005
MANUFACTURING METHOD)
THEREOF, AND HIGHLY INTEGRATED)
MODULE AND MANUFACTURING)
METHOD THEREOF)

AMENDMENT

Mail Stop Non-Fee Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In response to the Office Action dated May 2, 2005, please amend the above identified application as follows.

Certificate of Mailing - 37 CFR 1.8(a)
I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to:
Commissioner for Patents, P.O. Box 1450
Alexandria, VA 22313-1450, on the date below.

Date 9/2

